	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments	Error Definition	Errors
1	BRS	L1	14	usb and microcomputer and interfac$3 and reset and (system adj1 clock)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/02/24 16:45			0
2	BRS	L2	93	usb and (microcomputer or microprocessor) and interfac$3 and reset and (system adj1 clock)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/02/24 16:46			0
3	BRS	L3	88	state and 2	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/02/24 16:47			0
4	BRS	L4	8	((off or non-connection) near state) and 2	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/02/24 17:01			0
5	BRS	L5	22	(oscillator$1) and 2	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/02/24 17:14			0
6	BRS	L6	3	4 and 5	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/02/24 17:17			0
7	BRS	L7	635	331/46,49.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/02/24 17:18			0
8	BRS	L8	277	331/46.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/02/24 17:17			0
9	BRS	L9	1	2 and 7	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/02/24 17:18			0
10	BRS	L10	1	usb and 7	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/02/24 17:19			0